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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/630,258	08/01/2000	Marc Hoffman	ADI-005XX	7200

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EXAMINER	
DO, CHAT C	

ART UNIT	PAPER NUMBER
2124	

DATE MAILED: 08/03/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action	Application No.	Applicant(s)	
	09/630,258	HOFFMAN ET AL.	
	Examiner	Art Unit	
	Chat C. Do	2124	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 21 June 2004 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

PERIOD FOR REPLY [check either a) or b)]

a) ☒ The period for reply expires *4* months from the mailing date of the final rejection.

b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.
ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on _____. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.

2. ☒ The proposed amendment(s) will not be entered because:

   (a) ☒ they raise new issues that would require further consideration and/or search (see NOTE below);

   (b) ☐ they raise the issue of new matter (see Note below);

   (c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or

   (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

   NOTE: *See below*.

3. ☐ Applicant's reply has overcome the following rejection(s): _____.

4. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).

5. ☒ The a)☐ affidavit, b)☐ exhibit, or c)☒ request for reconsideration has been considered but does NOT place the application in condition for allowance because: *See below*.

6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.

7. ☒ For purposes of Appeal, the proposed amendment(s) a)☒ will not be entered or b)☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

   The status of the claim(s) is (or will be) as follows:

   Claim(s) allowed: _____.

   Claim(s) objected to: _____.

   Claim(s) rejected: *1-8*.

   Claim(s) withdrawn from consideration: _____.

8. ☐ The drawing correction filed on _____ is a)☐ approved or b)☐ disapproved by the Examiner.

9. ☐ Note the attached Information Disclosure Statement(s)(PTO-1449) Paper No(s). _____.

10. ☐ Other: _____

ANIL KHATRI
PRIMARY EXAMINER

Part 2(a): The applicant had amended the independent claims which included the limitations "predetermined number R" and process of transform "in a plurality of computation stages" in claim 1, the limitations "to perform said second ...writing operations are performed" in lines 9-13 page 5 in claims 5 and 8. These amendments to the claims raise new issues that would required further consideration and search in order to make a solid decision.

Part 5(c): Based on the last version of claims, Nakai et al. disclose a method of computing a FFT in Figures 1-22 (first embodiment), the method comprising: (a) receiving N time-ordered first data values (Figure 3 discloses the data input arrive in time-order for every symbol x(0)-x(N-1) and Figure 7 FFT processing [i+2]);(b) sequentially storing in a first memory each of N time-ordered first data values (Figure 3 RAM#0 and col. 8 lines 30-32) in the time order (and Figure 7 FFT processing [i+2]);(c) storing in a second memory a plurality of twiddle factors in a bit reversed order (104 in Figure 1 and Figure 8); (d) reading R input butterfly data values of N first data values wherein R butterfly data values are separated by N/R first data value in N time-ordered first data value (N = 32, R = 4, and separated by 8 different groups of input data); (e) performing a radix R butter fly calculation on R butterfly input data using at least one fo the plurality of twiddle factors stored in the second memory to generate R butterfly output data values(Figure 4 stage 0, this is a standard method of implementing FFT, the left data are the data that read from the RAM#0 using RAM address generator); (f) sequentially storing R butterfly output data values in sequential memory locations of a third memory (RAM#1 and col. 8 lines 30-32); and (g) performing steps (c) to (f) N/R x 2 times (compute other groups 1-7 in Figure 4)wherein reading step (d) includes reading the R butterfly data values from third memory (RAM #1 and col. 8 lines 30-32). Nakai et al. do not disclose the memory store operation performed in storing step (f) has a unity stride, thereby allowing R butterfly data values to be read from contiguous memory locations each time the R butterfly data values are read from third memory. However, Witek et al. disclose the advantage and operations of loading and storing operations in a unity stride whenever the storing is unity stride, the stored elements are stored contiguously in memory for ease of accessing and loading (col. 12 lines 17-25 and Figure 9). Therefore, it would have been obvious to a person having ordinary skill in the art at the time the invention is made to add the memory storing the results of operations performed in step (f) has a unity stride as seen in Witek et al.'s invention into Nakai et al.'s invention because it would enable to load or access the stored elements in a memory efficiently (col. 12 lines 17-25).